



SHADAN WOMEN'S COLLEGE OF ENGINEERING AND TECHNOLOGY

(An UGC Autonomous Institution)

Approved By AICTE | Affiliated to JNTUH | Accredited by NAAC with 'B++' Grade
6-2-980, Raj Bhavan Road, Khairatabad, Hyderabad, Telangana—500004

II YEAR B. TECH — I SEMESTER— REGULAR/SUPPLEMENTARY EXAMINATIONS DEC/JAN-2025

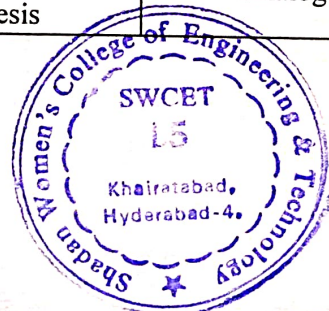
BTECH II YEAR I SEMESTER — R23 REGULATIONS

Timings — 10:00 AM to 1:00 PM

Date: 08-DEC-2025

| BRANCH | DATE, SESSION & DAY | | | | |
|---|--------------------------------|------------------------------|---|--|--|
| | 22-12-2025 (FN) Monday | 24-12-2025 (FN) Wednesday | 29-12-2025 (FN) Monday | 31-12-2025 (FN) Wednesday | 03-01-2026 (FN) Saturday |
| Computer Science and Engineering | Digital Electronics | Data Structures | Computer Oriented Statistical Methods | Computer Organization and Architecture | Object Oriented Programming through Java |
| Information Technology | Digital Electronics | Data Structures | Computer Oriented Statistical Methods | Computer Organization and Microprocessor | Introduction to IoT |
| Computer Science and Engineering (Data Science) | Digital Electronics | Data Structures | Statistical methods for Data Science | Computer Organization and Architecture | Object Oriented Programming through Java |
| Computer Science and Engineering (AI & ML) | Software Engineering | Data Structures | Statistical Foundations for Artificial Intelligence | Computer Organization and Architecture | Operating Systems |
| Artificial Intelligence and Data Science | Digital Electronics | Data Structures | Mathematical and Statistical Foundations | Computer Organization and Architecture | Object Oriented Programming through Java |
| Electronics and Communication Engineering | Network analysis and Synthesis | Analog Circuits | Numerical Methods and Complex Variables | Digital Logic Design | Signals and Systems |

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